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Attorney's Docket No. S1022/8707



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ferruccio FRISINA
Serial No.: Unassigned
Filing Date: August 8, 2001
For: METHOD OF MANUFACTURING AN INTEGRATED EDGE STRUCTURE
FOR HIGH VOLTAGE SEMICONDUCTOR DEVICES, AND RELATED
INTEGRATED EDGE STRUCTURE

Examiner: Unassigned
Art Unit: Unassigned

Commissioner for Patents
Washington, D.C. 20231

STATEMENT FILED PURSUANT TO THE DUTY OF
DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement is being filed concurrently with the filing of a National Application. No fee or certification is required

Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references. The examiner's attention is hereby drawn to the attached European Search Report.

Remarks

Documents cited on the attached form PTO-1449 (modified) are enclosed unless otherwise indicated on the attached form PTO-1449 (modified). It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

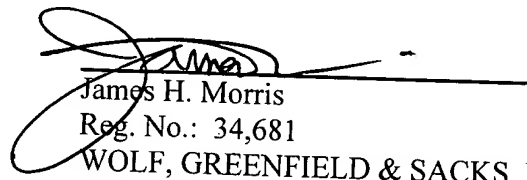
By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,

FERRUCCIO FRISINA


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FORM PTO-1449/A and B (Modified) INFORMATION DISCLOSURE STATEMENT BY APPLICANT			APPLICATION NO.:		ATTY. DOCKET NO.: S1022/8707	
			FILING DATE: August 8, 2001			
			APPLICANT: Ferruccio FRISINA			
			GROUP ART UNIT:		EXAMINER:	
Sheet	1	Of	1			

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 09/925080
 08/08/01

U.S. PATENT DOCUMENTS

Examiner's Initials#	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YY
		Number	Kind Code		
		6010926		Rho et al.	01/04/00
		5963798		Kim et al.	10/05/99
		5633180		Bajor	05/27/97
		5900662		Frisina et al.	05/04/99
		5798187		Wilson et al.	08/25/98

FOREIGN PATENT DOCUMENTS

Examiner's Initials#	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YY	Translation (Y/N)	
		Office/Country	Number	Kind Code				
		EP	0 757 382	A	Consorzio Per La Ricerca Sulla	02/05/97		
		EP	0 413 256	A	Motorola, Inc.	02/20/91		
		GB	2 163 597	A	SGS-ATES Componenti Elettronici	02/26/86		

OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner's Initials#	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		European Search Report from European Patent Application 98830739.3, filed December 9, 1998	
		Patent Abstracts of Japan, vol. 006, no. 264 (E-150), December 23, 1982 & JP-A-57 159060 (Nippon Denki KK)	
		Patent Abstracts of Japan, vol. 008, no. 081 (E-238), April 13, 1984 & JP-A-59 002344 (Nippon Denki KK)	
		Patent Abstracts of Japan, vol. 014, no. 036 (E-877), January 23, 1990 & JP-A-01 270346 (Fuji Electric Co. Ltd.)	

EXAMINER	DATE CONSIDERED
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. _____, filed _____, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - Must provide a copy of any patent, publication, other information listed, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]